

**Remarks/Arguments:**

Claims 1-12 are pending in the above-identified application. Claims 1-12 were rejected under 35 U.S.C. 112, second paragraph, as being indefinite or failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. This ground for rejection is overcome by the amendments to claims 1-12.

The Examiner's objection to Figures 1-5, under MPEP § 608.02(g), is overcome by the amended drawings, transmitted herewith.

Claims 1-12 were additionally rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's prior art figures 1-5. Applicant's figure 1 represents a schematic view of the thyristor array and is also found as figure 3 of Japanese patent JP3194978. Applicant's figure 2 represents a perspective view of figure 1. Applicant's figures 3-5 represent schematic views of the thyristor array wiring that include crossovers and that illustrate the problem solved by the invention of claims 1-12. Applicant's figures 6-12 illustrate novel solutions (claims 1-12) to the crossover wiring problems seen in figures 3-5. Applicant's figures 1-5 are schematic diagrams, not circuit layouts. While these figures show the problem of cross over wiring, they do not disclose or suggest any solution.

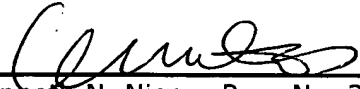
Applicant's prior art, Japanese patent JP3194978, is submitted herewith. The solution to the cross over problem described therein involves using a double parallel array of conducting buses that serve to eliminate the crossover wiring, as seen in figure 4 of JP3194978. Claims 1-12 in the subject application solve this problem of cross over wiring in more economical and elegant methods, dissimilar to those presented in JP3194978. Accordingly, claims 1-12 are not subject to rejection under 35 U.S.C. 103(a) as being obvious in view of figures 1-5.

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Amendment Dated March 27, 2003  
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In light of the foregoing amendments and remarks, Applicant respectfully requests that the Examiner reconsider and withdraw the objections to the drawings and the rejection of claims 1-12.

Respectfully submitted,

  
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Attachments: Figure(s) 1-5 (5 sheets)

Dated: March 27, 2003

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The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. **18-0350** of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on:

March 27, 2003

  
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